

## WEST Search History

DATE: Thursday, April 14, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L16	L15 AND ROW\$ AND COLUMN\$	17
<input type="checkbox"/>	L15	L14 AND CELL\$	19
<input type="checkbox"/>	L14	L13 AND ACCESS TRANSISTOR	19
<input type="checkbox"/>	L13	L12 AND SENSE AMPLIFIER	122
<input type="checkbox"/>	L12	RESISTIVE ADJ4 MEMORY AND (SLICES\$ OR PLANES\$ OR LAYERS\$)	398
		<i>DB=EPAB,JPAB; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L11	L9 AND ACCESS TRANSISTOR	1
<input type="checkbox"/>	L10	L9 AND SENS\$	2
<input type="checkbox"/>	L9	RESISTIVE ADJ6 MEMORY AND (SLICES\$ OR PLANES\$ OR LAYER\$)	25
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L8	L7 AND (SLICES\$ OR PLANES\$ OR LAYERS\$)	29
<input type="checkbox"/>	L7	L6 AND CELL\$	36
<input type="checkbox"/>	L6	L5 AND ADDRESS\$	36
<input type="checkbox"/>	L5	L4 AND ACCESS TRANSISTOR	45
<input type="checkbox"/>	L4	L3 AND ARRAY	291
<input type="checkbox"/>	L3	L2 AND SENS\$	307
<input type="checkbox"/>	L2	L1 AND ROW\$ AND COLUMN\$	332
<input type="checkbox"/>	L1	RESISTIVE ADJ4 MEMORY	569

END OF SEARCH HISTORY